

Abstracts

A Fully Integrated 35 GHz MMIC Receiver with On-Chip LO

R. Ramachandran, C. Woo, M. Nijjar and D. Fisher. "A Fully Integrated 35 GHz MMIC Receiver with On-Chip LO." 1993 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 93.1 (1993 [MCS]): 93-96.

A fully integrated 35 GHz MMIC receiver chip has been designed, fabricated and tested. The chip contains a Low-Noise Amplifier (LNA), an active mixer, an IF amplifier, a local oscillator, and a buffer amplifier. The only external component required is the resonator. This feature was incorporated to accommodate both DRO and VCO implementations. Chip size is 54 x 80 mils (1.35 x 2 mm).

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